

#5/A S.W.H. 10/08/02

Docket No.: 62807-038

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of  
Shinobu TORIKOSHI, et al.

Serial No.: 10/076,143

Filed: February 15, 2002

Group Art Unit: 2631

Examiner: Unknown

RECEIVED  
SEP 24 2002  
Technology Center 2600

For: DIGITAL SIGNAL PROCESSING DEVICE, DV DECODER, RECORDING  
DEVICE USING DV DECODER, AND SIGNAL PROCESSING METHOD

PRELIMINARY AMENDMENT

Commissioner for Patents  
Washington, DC 20231

Sir:

Prior to examination of the above-referenced application, please amend the  
application as follows:

IN THE CLAIMS:

Please cancel claims 1-21.

Please add new claims 22-42 as follows.

A/  
CONT'D

22. (New) A digital signal processing device for processing an input video signal  
and an input audio signal inputted with time information for synchronization and  
outputting the processed signals, comprising:

a clock generator for generating a reference clock independent of the time  
information for synchronization;

a video signal processing unit, responsive to the clock generator, for

09/18/2002 KZENDIE 00000087 500417 10076143  
01 FC:102 168.00 CH